

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

SEONG-JAE LEE, ET AL.

Application No.: 10/033,394

Filed: December 28, 2001

For: **METHOD OF FABRICATING  
INTEGRATED CIRCUIT HAVING  
SHALLOW JUNCTION - UTILITY**

Art Group:

Examiner:

COPY OF PAPERS  
ORIGINALLY FILED**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to Rule 1.97, Applicant desires to make of record the reference(s) set forth on the attached Form PTO 1449 (or PTO/SB/08). A copy of each reference is submitted herewith.

It is hereby stated that this Information Disclosure Statement is being filed within three months of the filing date of the subject application, therefore no petition or fee is required. However, in the event a petition is needed for consideration of this Information Disclosure Statement, Applicant hereby so petitions. Please charge any additional fee due to Deposit Account 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR &amp; ZAFMAN LLP

Date: *3/25/02*

Eric S. Hyman, Reg. No. 30,139

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 to the Patent and Trademark Office on: *3-26-02*

*Melissa Stead*  
 Melissa Stead

*3-26-02*  
 Date